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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/033,525	12/28/2001	Rajasekhar Pullala	1298/1F986-US2	7823
27964	7590	06/22/2004	EXAMINER	
HITT GAINES P.C. P.O. BOX 832570 RICHARDSON, TX 75083			LE, DINH THANH	
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 06/22/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Applicant(s) PULLELA ET AL.
	Art Unit 2816

Application No.

10/033,525

Examiner

DINH T. LE

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on 4/21/04.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 7-17 is/are allowed.
- 6) ☒ Claim(s) 1,2,6,14 and 16-18 is/are rejected.
- 7) ☒ Claim(s) 3-5 and 15 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____. | 6) <input type="checkbox"/> Other: _____. |

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NON-FINAL REJECTION

Response to Applicant's Amendment

The rejection over Harada et al (US 6,023,363) is withdrawn.

A newly found prior art necessitated a new ground of rejection as below:

Claim Rejections

Claim Rejections - 35 USC § 112

Claims 17-18 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Correction or clarification is required.

With regard to claim 17, it is unclear how the admittance stage can be “configured” to produce a “data output” and how this limitation is read on the preferred embodiment. Insofar as understood, no such “data output” is seen on the drawings.

In claim 18, it is how the transistors can lack a common coupling point, what the common coupling point is and how it is read on the preferred embodiment or seen on the drawings.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-2, 6, 14, 16 and 18-19 are rejected under 35 USC 102 (b) as being anticipated by Benedict et al (US 4,276,488).

Benedict et al discloses in Figure 2 a master slave circuit comprising:

- a clocked trans-admittance stage circuit (33-37) for receiving a voltage (DIN, D12) and producing a current output (Ia); and
- an active load (40-43) connected to receive as input the current output (Ia) of said trans-admittance circuit and produce a voltage output. (QMI).
- transmission lines (connecting wires) coupled between said clocked trans-admittance circuit (33-37) and said active load (40-43).
- wherein the active load (40-43) is a trans- impedance stage circuit.

With regard to claim 14, a first combined trans-admittance and trans-impedance stage and a second combined trans-admittance and trans-impedance stage are anticipated the master latch (20-1) and a slave (21) in Figure 2 of Benedict et al.

With regard to claim 16, the first and said second combined state are clocked on opposite phase of clock signal by the level shift (24).

Allowable Subject Matter

Claims 7-13 are allowable because the prior art does not disclose at least one latch pair for generating an additional output current.

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Claims 3-5 and 15 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims. The claims would be allowable because the prior art does not show a first pair of transistors, a second pair of transistors and a third pair of transistors as claimed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Dinh Le whose telephone number is (703) 305-3790. The examiner can normally be reached on Monday to Friday from 7:00 A.M. to 5:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim Callahan, can be reached on (703) 308-4876. The fax phone number for this Group is (703) 308-7722.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

DINH LE
Primary Examiner



June 16, 2004